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[AbstractPlus](#) | Full Text: [PDF\(136 KB\)](#) IEEE CNF

IEEE STD IEEE Standard

2. **4Mb on-chip-cache bubble memory chips with 4 µm period ion-implanted propag**
Urai, H.; Mizuno, K.; Asada, S.; Takada, N.; Yoshioka, N.; Gokan, H.; Yoshimi, K.; Magnetics, IEEE Transactions on Volume 20, Issue 5, Sep 1984 Page(s):1072 - 1074
[AbstractPlus](#) | Full Text: [PDF\(552 KB\)](#) IEEE JNL

3. **Hierarchical fault tolerance for 3D microelectronics**
Campbell, M.; Little, M.; Yung, M.; Wafer Scale Integration, 1990. Proceedings., [2nd] International Conference on 23-25 Jan. 1990 Page(s):174 - 188
[AbstractPlus](#) | Full Text: [PDF\(840 KB\)](#) IEEE CNF

4. **Fault tolerance as an aspect using JReplica**
Herrero, J.L.; Sanchez, F.; Toro, M.; Distributed Computing Systems, 2001. FTDCS 2001. Proceedings. The Eighth IEEE W Future Trends of 31 Oct.-2 Nov. 2001 Page(s):201 - 207
[AbstractPlus](#) | Full Text: [PDF\(99 KB\)](#) IEEE CNF

5. **Control function margin degradation in a bubble memory chip**
Yamaguchi, N.; Magnetics, IEEE Transactions on Volume 14, Issue 3, May 1978 Page(s):128 - 129
[AbstractPlus](#) | Full Text: [PDF\(264 KB\)](#) IEEE JNL

6. **General framework for fault tolerance from ISO/ITU Reference Model for Open Di Processing (RM-ODP)**
Putman, J.; Object-Oriented Real-Time Dependable Systems, 1999. WORDS 1999 Fall. Proceedin International Workshop on

18-20 Nov. 1999 Page(s):111 - 118

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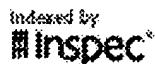
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Doherty, B.S.; Harris, S.A.J.;

Education, IEEE Transactions on

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1. Single-shot optical sampling oscilloscope for ultrafast optical waveforms

Kung-Li Deng; Runser, R.J.; Glesk, I.; Prucnal, P.R.;
Photonics Technology Letters, IEEE
Volume 10, Issue 3, March 1998 Page(s):397 - 399

[AbstractPlus](#) | [References](#) | [Full Text: PDF\(56 KB\)](#) IEEE JNL**2. Real-time optimisation for power generation and CHP**

Dormer, A.R.;
Power Engineering Journal [see also Power Engineer]
Volume 6, Issue 2, March 1992 Page(s):95 - 100

[AbstractPlus](#) | [Full Text: PDF\(332 KB\)](#) IEE JNL**3. General framework for fault tolerance from ISO/ITU Reference Model for Open Di- Processing (RM-ODP)**

Putman, J.;
Object-Oriented Real-Time Dependable Systems, 1999. WORDS 1999 Fall. Proceedin International Workshop on
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1984. (INZZ) 4 Mb on-chip-cache bubble memory chips with 4 mu m period ion- implanted propagation patterns.

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5080285, C9511-1290L-042; 951010.

TitleCompartmentation in **replicator** models.**Author(s)**[Nuno-J-C](#); [Chacon-P](#); [Moreno-A](#); [Moran-F](#); [Ed. by Moran-F](#); [Moreno-A](#); [Merelo-J-J](#); [Chacon-P](#).**Author affiliation**

ETSI de Matematicas, Univ Politecnica de Madrid, Spain.

Source

Advances in Artificial Life. Third European Conference on Artificial Life. Proceedings, Granada, Spain, 4-6 June 1995.

In: p.116-27, 1995.

ISSN

ISBN: 3-540-59496-5.

Publication year

1995.

Language

EN.

Publication type

CPP Conference Paper.

Treatment codes

P Practical; T Theoretical or Mathematical.

Abstract

Recently a model formed by self-replicative units with catalytic capabilities evolving in an extended system has been presented. It has been shown that under particular conditions this model exhibits spatial compartmentation without any kind of membrane. In the framework of ALife, we suggest that this model can allow a global growth in the complexity of those models based on the hypothesis of the so-called RNA-world. However, this increase has got a limit defined by the impossibility of expressing the informational potentialities into **functional** complexity when a unique type of entity is involved in the system. (28 refs).

Descriptors[biology](#); [computational-complexity](#); [evolution-biological](#).**Keywords**

replicator models; compartmentation; self replicative units; catalytic capabilities; ALife; complexity; RNA world; informational potentialities; **functional** complexity; entity.

Classification codes

C1290L (Systems theory applications in biology and medicine).

C4240C (Computational complexity).

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File: PGPB

Jun 5, 2003

PGPUB-DOCUMENT-NUMBER: 20030105620

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030105620 A1

TITLE: System, method and article of manufacture for interface constructs in a programming language capable of programming hardware architectures

PUBLICATION-DATE: June 5, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bowen, Matt	Littlemore		GB	

US-CL-CURRENT: 703/22

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn D](#)

2. Document ID: US 20030074177 A1

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File: PGPB

Apr 17, 2003

PGPUB-DOCUMENT-NUMBER: 20030074177

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030074177 A1

TITLE: System, method and article of manufacture for a simulator plug-in for co-simulation purposes

PUBLICATION-DATE: April 17, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bowen, Matt	Oxford		GB	

US-CL-CURRENT: 703/22

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn D](#)

3. Document ID: US 20030046671 A1

L4: Entry 3 of 10

File: PGPB

Mar 6, 2003

PGPUB-DOCUMENT-NUMBER: 20030046671
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030046671 A1

TITLE: System, method and article of manufacture for signal constructs in a programming language capable of programming hardware architectures

PUBLICATION-DATE: March 6, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bowen, Matt	Oxford		GB	

US-CL-CURRENT: 717/141

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KINDC](#) | [Drawn D](#)

4. Document ID: US 20030046668 A1

L4: Entry 4 of 10

File: PGPB

Mar 6, 2003

PGPUB-DOCUMENT-NUMBER: 20030046668
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030046668 A1

TITLE: System, method and article of manufacture for distributing IP cores

PUBLICATION-DATE: March 6, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bowen, Matt	Littlemore		DE	

US-CL-CURRENT: 717/131

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5. Document ID: US 20030037321 A1

L4: Entry 5 of 10

File: PGPB

Feb 20, 2003

PGPUB-DOCUMENT-NUMBER: 20030037321
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030037321 A1

TITLE: System, method and article of manufacture for extensions in a programming

lanauage capable of programming hardware architectures

PUBLICATION-DATE: February 20, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bowen, Matt	Oxford		GB	

US-CL-CURRENT: 717/149

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMD](#) | [Drawn](#) | [De](#)

6. Document ID: US 20030033594 A1

L4: Entry 6 of 10

File: PGPB

Feb 13, 2003

PGPUB-DOCUMENT-NUMBER: 20030033594

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030033594 A1

TITLE: System, method and article of manufacture for parameterized expression libraries

PUBLICATION-DATE: February 13, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bowen, Matt	Oxford		GB	

US-CL-CURRENT: 717/141; 717/163

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7. Document ID: US 20030033588 A1

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File: PGPB

Feb 13, 2003

PGPUB-DOCUMENT-NUMBER: 20030033588

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030033588 A1

TITLE: System, method and article of manufacture for using a library map to create and maintain IP cores effectively

PUBLICATION-DATE: February 13, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Alexander, John	Oxfordshire		GB	

US-CL-CURRENT: 717/107; 717/163

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KINIC	Drawn D
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PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030028864 A1

TITLE: System, method and article of manufacture for successive compilations using incomplete parameters

PUBLICATION-DATE: February 6, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bowen, Matt	Oxford		GB	

US-CL-CURRENT: 717/141

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File: PGPB

Dec 26, 2002

PGPUB-DOCUMENT-NUMBER: 20020199173

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020199173 A1

TITLE: System, method and article of manufacture for a debugger capable of operating across multiple threads and lock domains

PUBLICATION-DATE: December 26, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bowen, Matt	Oxford		GB	

US-CL-CURRENT: 717/129; 717/131

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L4: Entry 10 of 10

File: USPT

Feb 10, 2004

US-PAT-NO: 6691301

DOCUMENT-IDENTIFIER: US 6691301 B2

TITLE: System, method and article of manufacture for signal constructs in a programming language capable of programming hardware architectures

DATE-ISSUED: February 10, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Bowen; Matt	Littlemore			GB

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PGPUB-DOCUMENT-NUMBER: 20030105620

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030105620 A1

TITLE: System, method and article of manufacture for interface constructs in a programming language capable of programming hardware architectures

PUBLICATION-DATE: June 5, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bowen, Matt	Littlemore		GB	

US-CL-CURRENT: 703/22

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File: PGPB

Apr 17, 2003

PGPUB-DOCUMENT-NUMBER: 20030074177

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030074177 A1

TITLE: System, method and article of manufacture for a simulator plug-in for co-simulation purposes

PUBLICATION-DATE: April 17, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bowen, Matt	Oxford		GB	

US-CL-CURRENT: 703/22

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KINIC	Drawn D
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PGPUB-DOCUMENT-NUMBER: 20030046671

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030046671 A1

TITLE: System, method and article of manufacture for signal constructs in a programming language capable of programming hardware architectures

PUBLICATION-DATE: March 6, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bowen, Matt	Oxford		GB	

US-CL-CURRENT: 717/141

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KINIC	Drawn D
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4. Document ID: US 20030046668 A1

L6: Entry 4 of 15

File: PGPB

Mar 6, 2003

PGPUB-DOCUMENT-NUMBER: 20030046668

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030046668 A1

TITLE: System, method and article of manufacture for distributing IP cores

PUBLICATION-DATE: March 6, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bowen, Matt	Littlemore		DE	

US-CL-CURRENT: 717/131

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KINIC	Drawn D
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5. Document ID: US 20030037321 A1

L6: Entry 5 of 15

File: PGPB

Feb 20, 2003

PGPUB-DOCUMENT-NUMBER: 20030037321

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030037321 A1

TITLE: System, method and article of manufacture for extensions in a programming language capable of programming hardware architectures

PUBLICATION-DATE: February 20, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bowen, Matt	Oxford		GB	

US-CL-CURRENT: 717/149

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KINIC](#) | [Drawn D](#)

6. Document ID: US 20030033594 A1

L6: Entry 6 of 15

File: PGPB

Feb 13, 2003

PGPUB-DOCUMENT-NUMBER: 20030033594

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030033594 A1

TITLE: System, method and article of manufacture for parameterized expression libraries

PUBLICATION-DATE: February 13, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bowen, Matt	Oxford		GB	

US-CL-CURRENT: 717/141; 717/163

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KINIC](#) | [Drawn D](#)

7. Document ID: US 20030033588 A1

L6: Entry 7 of 15

File: PGPB

Feb 13, 2003

PGPUB-DOCUMENT-NUMBER: 20030033588

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030033588 A1

TITLE: System, method and article of manufacture for using a library map to create and maintain IP cores effectively

PUBLICATION-DATE: February 13, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
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L6: Entry 10 of 15

File: PGPB

Dec 26, 2002

PGPUB-DOCUMENT-NUMBER: 20020199173
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020199173 A1

TITLE: System, method and article of manufacture for a debugger capable of operating across multiple threads and lock domains

PUBLICATION-DATE: December 26, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bowen, Matt	Oxford		GB	

US-CL-CURRENT: 717/129; 717/131

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KWMC](#) | [Drawn D](#)

11. Document ID: US 6691301 B2

L6: Entry 11 of 15

File: USPT

Feb 10, 2004

US-PAT-NO: 6691301
DOCUMENT-IDENTIFIER: US 6691301 B2

TITLE: System, method and article of manufacture for signal constructs in a programming language capable of programming hardware architectures

DATE-ISSUED: February 10, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Bowen, Matt	Littlemore			GB

US-CL-CURRENT: 717/114; 712/15, 716/16

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KWMC](#) | [Drawn D](#)

12. Document ID: US 6044215 A

L6: Entry 12 of 15

File: USPT

Mar 28, 2000

US-PAT-NO: 6044215
DOCUMENT-IDENTIFIER: US 6044215 A

TITLE: System and method for expansion of a computer

DATE-ISSUED: March 28, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Charles; Paul	Morgan Hill	CA		
Peschke; Walter C.	Morgan Hill	CA		

US-CL-CURRENT: 703/25; 710/303

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | | | | [Claims](#) | [KMC](#) | [Drawn Ds](#)

13. Document ID: US 5390282 A

L6: Entry 13 of 15

File: USPT

Feb 14, 1995

US-PAT-NO: 5390282

DOCUMENT-IDENTIFIER: US 5390282 A

TITLE: Process for problem solving using spontaneously emergent self-replicating and self-improving entities

DATE-ISSUED: February 14, 1995

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Koza; John R.	Los Altos Hills	CA	94022	
Rice; James P.	Redwood City	CA		

US-CL-CURRENT: 706/13

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | | | | [Claims](#) | [KMC](#) | [Drawn Ds](#)

14. Document ID: US 4930102 A